

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO.	GROUP ART UNIT	ATTACHMENT TO PAPER NUMBER	13	
NOTICE OF REFERENCES CITED				09-682,863 2817 APPLICANT(S) McGrath				
U.S. PATENT DOCUMENTS								
•	DOCUMENT NO.	DATE	NAME		CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE	
A	6344663	2-5-2002	Slater, Jr et al.		257	77		
B								
C								
D								
E								
F								
G								
H								
I								
J								
K								
FOREIGN PATENT DOCUMENTS								
•	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG	PP. SPEC.
L	130608	8-4-1983	Japan	Onishi et al.	—	—		
M								
N								
O								
P								
Q								
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)								
R	Palmour et al. "High-temperature depletion-mode metal-oxide-semiconductor field-effect transistors in beta-SiC thin films" Appl. Phys. Lett. 51, 14 December 1987 pp 2028-2030							
T	Schmid et al. "Process technology and high temperature performance of 6H-SiC MOS devices" The Third European Conference on High temperature Electronics 1999 pp 195-197							
EXAMINER MICHAEL B SHINGLETON		DATE 4-3-03						
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)								